DON BOSCO COLLEGE OF ENGINEERING , FATORDA - GOA.

Examination Result of Semester III Regular Examination December/January 2023-24 (RC 2019-20)

SE(Electronics and Computer Science) R E S U L T

| Sr. No | Seat No | DBCE REG. No | Name of the Candidate | Permanent Registration Number | Essential Mathematics for Engineers | | | | | | | Electrical Circuits & Systems | | | | | | | Electronic Devices and Circuits | | | | | | | | | Digital Electronics | | | | | | | | Data Structures and Algorithms using C++ | | | | | | | Electronic Devices and Circuits Lab | | | | | | Data Structures and Algorithms using C++ Lab | | | | | | Digital Electronics Lab | | | | | | Technical Writing and Professional Communication | | | | | | Bridge Cour | | Entitlement marks | | | | | Total Credits | | Semester III | | |
|---|---|---

DON BOSCO COLLEGE OF ENGINEERING , FATORDA - GOA.

Examination Result of Semester III Regular Examination December/January 2023-24 (RC 2019-20)

SE(Electronics and Computer Science) R E S U L T

| Sr. No | Seat No | DBCE REG. No | Name of the Candidate | Permanent Registration Number | Essential Mathematics for Engineers | | | | | | | | Electrical Circuits & Systems | | | | | | | | Electronic Devices and Circuits | | | | | | | | | | Digital Electronics | | | | | | | | | | Data Structures and Algorithms using C++ | | | | | | | | Electronic Devices and Circuits Lab | | | | | | | | Data Structures and Algorithms using C++ Lab | | | | | | | | Digital Electronics Lab | | | | | | | | Technical Writing and Professional Communication | | | | | | | | Bridge Cour | | Entitlement marks | | | | | Total Credits | | Semester III | | |
|---|---|---